

GR 97 P 2681



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By: Mr. P. ~

Date: 2/26/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Albrecht Mayer

Applic. No. : 09/164,123

Filed : September 30, 1998

Title : Method for Assembling Integrated Circuits
With Protection Of The Circuits Against
Electrostatic Discharge, And Arrangement Of
Integrated Circuits With Protection Against
Electrostatic Discharge

Examiner : Kurt M. Eaton

Group Art Unit : 2823

A M E N D M E N T under 37 C.F.R. § 1.116

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the final Office action dated September 26, 2000

kindly amend the above-identified application as follows:

In the Claims:

Please cancel claim 15.

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Kurt Eaton
2/26/01